

INFINITE IMPULSE RESPONSE DIGITAL FILTER FOR PULSE SIGNAL PERIODS

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Abstract

This article describes a new kind of IIR (Infinite Impulse Response) digital filter, which is intended for the filtering of impulse periods. This filter is by nature a Frequency Locked Loop (FLL), which is based on the measurement and processing of the input and output periods. The paper shows how a digital filter can be designed from one FLL using the theory of classical IIR digital filters. All mathematical analyzes were performed using Z transformation and the theory of linear discrete systems. To illustrate this approach a Butterworth IIR digital filter is designed, which is based on a third-order FLL. The filtering ability of the third-order FLL is demonstrated for one practical example.

Keywords: Digital circuits, Digital filters, PLL, FLL, Pulse circuits, Linear discrete system.

INTRODUCTION

The field of the special type of Frequency Locked Loop (FLL), recently described in the literature, which is based on the measurement and processing of the pulse signal periods, has acquired its new, very important role in theory and practice. Namely, for a special choice of FLL parameters, this system becomes a digital filter like a classic digital filter, but such a system filters the pulse signal periods. It was shown in ref. [1] that, Mat-lab tools intended for the classical Finite Impulse Response (FIR) digital filters, can be used for the frequency analysis of FLL, which are based on the measurement and processing of the input signal periods only. Using the theory and tools of the classical FIR digital filters, it was demonstrated in ref. [2], how a new type of FIR digital filters can be designed from a period FLL. In refs. [1] and [2], the described FLLs process only the input signal periods. In accordance with the already known term from the theory of FIR digital filters, in which the filter processes only the samples of the input signal, we called the described filters FIR FLL digital filters.

This paper describes a methodology for designing a FLL digital filter that processes both, the input and output signal periods. In accordance with the adopted terminology from the theory of the Infinite Impulse Response (IIR) digital filters, let's call these systems the IIR FLL digital filters.

The refs. [3] to [11] describe the period FLLs and period Phase Locked Loops (PLLs), whose construction and functioning are the same as the FIR FLL and IIR FLL digital filters. However, for the different choice of parameters, they were applied in the field of frequency averaging, phase shifting, time shifting, phase control, tracking, predicting, frequency synthesizers, noise rejection, frequency multipliers and the others. The rest of articles and books [12] to [17] in the references, are used as the theoretical base.

DESIGN OF IIR FLL DIGITAL FILTER

For simplicity, the IIR FLL digital filter design will be demonstrated on the third-order FLL. The same principles that apply to the third-order FLL can be used on any higherorder FLL in order to design an IIR FLL digital filter. A general case of an input signal Sin and an output signal Sop of the third-order FLL is presented in Fig. 1. The periods TI_k and TO_k, as well as the time difference τ_k , occur at discrete times t_k , t_{k+1} , t_{k+2} , t_{k+3}, which are defined by the falling edges of the pulses of

Sop in Fig. 1. The input and output periods TI_k and TO_k as well as the time differences τ_k , are distributed in time in Fig. 1, so that every input period overlaps with the output period of the same order. Due to this distribution and overlapping in time, it is not possible to calculate, for instance, the output period TO_{k+3} as a function of TI_{k+3} , because the calculation of TO_{k+3} must be finished up to discrete time t_{k+3} , i.e. before the input period TI_{k+3} is expired, see Fig. 1. At discrete time t_{k+3} the realization of TO_{k+3} should start. In other word, in the real time applications, any output period can be calculated only using the previous input periods of the lower order. Taking this fact in account, the general difference equation which describes the thirdorder FLL, corresponding to Fig.1, is presented in eq. (1), where b_1 , b_2 , b_3 , a_1 , a_2 and a3 are the system parameters. Following the same idea, presented in ref. [2], that the FLL parameters should be changed by the coefficients of a classical digital filter, let us first compare eq. (1) with the difference equation of the second order FIR digital filter, which is presented in eq. (2). Note that y(k) is



Fig. 1. The time relations between the input and output periods and the time differences of the third-order FLL.

$$TO_{k+3} = b_1 TI_{k+2} + b_2 TI_{k+1} + b_3 TI_k + a_1 TO_{k+2} + a_2 TO_{k+1} + a_3 TO_k$$
(1)

$$a_{0d}y_{k+2} + a_{1d}y_{k+1} + a_{2d}y_{k} = b_{0d}x_{k+2} + b_{1d}x_{k+1} + b_{2d}x_{k}$$
(2)

the calculated output and x(k) is the sampled input in digital form. All coefficients in eq. (2) have suffix "d", to signify that they belong to the digital filter. Coefficient a_{0d} is always equal to one in a FIR digital filter. Note that in eq. (1), parameter a_0 , which should be attached to TO_{k+3} , is also equal to one. Equation (2) corresponds to the form recognized by Matlab, while eq. (1) corresponds to a form which is suitable for the mathematical analysis of a real-time FLL. Including a_{0d} and a₀, we notice that eq. (1) contains 3 parameters "b" and 4 parameters "a", while equation (3) contains 3 coefficients b_d and 3 coefficients "a_d". In order to equalize the number of parameters with the number of coefficients, we must omit parameter a_3 in eq. (1). Equation (1) changes to modified form, given by eq. (3). Another equation describing the natural relation between the input and output periods and time differences τ_k , which comes out from Fig. 1, is shown in eq. (4). In order to compare eqs. (2) and (3), let us write eq. (2) in the form, similar to eq. (3), shown in eq. (5).

$$TO_{k+3} = b_1 TI_{k+2} + b_2 TI_{k+1} + b_3 TI_{k} + a_1 TO_{k+2} + a_2 TO_{k+1}$$
(3)

$$\tau_{k+1} = \tau_k + TO_k - TI_k \tag{4}$$

$$y_{k+2} = b_{0d} x_{k+2} + b_{1d} x_{k+1} + b_{2d} x_{k} -a_{1d} y_{k+1} - a_{2d} y_{k}$$
(5)

To design an IIR FLL digital filter of the third order (IIR FLL₃) using the Mat-lab tools, we need to find the Z transforms of the transfer functions of the described FLL using equations (3) and (4). The Z transforms of eqs. (3) and (4) are presented in eqs. (6) and (7). In eqs. (6) and (7), TO₀, TI₀ and τ_0 are the initial conditions of the variables TO_k , TI_k and τ_k . Based on eq. (3), for k=-2, $TO_1=b_1TI_0+a_1TO_0$ and for k=-1, $TO_2=b_1TI_1+b_2TI_0+a_1TO_1+a_2TO_0$. Using the given expressions and eq. (6), TO(z)is calculated and shown in eq. (8), where $R(z)=z^{3}TO_{0}/(z^{3}-z^{2}a_{1}-za_{2})$. It is now of interest to investigate under which conditions this IIR FLL₃ is the stable system. To do that, let us suppose that the step input is TI(k) = TI =constant. Substituting the Z transform of TI(k) i.e. $TI(z) = TI \cdot z/(z-1)$ into eq. (8) and using the final value theorem, it is possible to find the final value of the output period TO_{∞} , which IIR FLL₃ reaches in the stable state. We can calculate $TO_{\infty} = \lim TO(k)$ if $k \rightarrow \infty$, using TO(z). This is shown in eq. (9). It comes out from eq. (9), that $TO_{\infty}=TI$ if eq. (10) is satisfied. Changing TO(z) given by eq. (8) into eq. (7), $\tau(z)$ is calculated and shown in eq.

(11), where $S_{ab}=b_2 + b_1 + a_2 + a_1-1$. Based on eqs. (8) and (11), we can define two transfer functions $H_{TO}(z)$ and $H\tau(z)=\tau(z)/TI(z)$, shown in eqs. (12) an (13).

$$z^{3}TO(z) - z^{3}TO_{0} - z^{2}TO_{1} - zTO_{2} = b_{1}[z^{2}TI(z) - z^{2}TI_{0} - zTI_{1}] + b_{2}[zTI(z) - zTI_{0}] + b_{3}TI(z) +$$
(6)
$$a_{1}[z^{2}TO(z) - z^{2}TO_{0} - zTO_{1}] + a_{2}[zTO(z) - zTO_{0}]$$

$$z\tau(z) - z\tau_0 = \tau(z) + TO(z) - TI(z)$$
(7)

$$TO(z) = TI(z)\frac{z^2b_1 + zb_2 + b_3}{z^3 - z^2a_1 - za_2} + R(z)$$
(8)

$$TO_{\infty} = \lim[(z-1)TO(z)]_{z \to 1} = TI \frac{b_1 + b_2 + b_3}{1 - a_1 - a_2}$$
 (9)

$$b_1 + b_2 + b_3 + a_1 + a_2 = 1 \tag{10}$$

$$\tau(z) = TI(z) \frac{-z^2 + z(b_2 + b_1 - 1) + S_{ab}}{z^3 - z^2 a_1 - za_2} + \frac{R(z) + z\tau_0}{z - 1} (11)$$

$$H_{TO}(z) = \frac{TO(z)}{TI(z)} = \frac{z^2 b_1 + z b_2 + b_3}{z^3 - z^2 a_1 - z a_2}$$
(12)

$$H_{\tau}(z) = \frac{-z^2 + z(b_2 + b_1 - 1) + b_2 + b_1 + a_2 + a_1 - 1}{z^3 - z^2 a_1 - z a_2}$$
(13)

Modified eq. (3) of FLL is structurally the same like eq. (5) and the next step is to simply change parameters in eq. (3) with the corresponding coefficients of the digital filter so that $b_1 = b_{0d}$, $b_2 = b_{1d}$, $b_3 = b_{2d}$, $a_1 = -a_{1d}$ and a_2 = - a_{2d} . The transfer function $H_{TO}(z)$, eq. (12) turns to eq. (14). The transfer function of the digital filter $H_{DF}(z)$, according to eq. (2) or eq. (5) is shown in eq. (15). The transfer functions $H_{TO}(z)$ and $H_{DT}(z)$ cover the same zeros and poles, but the difference between them is in their denominators. Namely, their relation can be expressed as $H_{TO}(z) = H_{DF}(z) \cdot z^{-1}$. This means that the magnitudes of the frequency responses of $H_{TO}(z)$ and $H_{DF}(z)$ will be the same. But due to one step delay, which refers to factor "z-1", IIR FLL₃ will introduce an additional delay of $-\pi$ [rad] on the output signal, in relation to the phase which the digital filter makes on its output signal. Based on the Mat-lab rules for definitions of vector "b" and "a" of the IIR digital filters, we can define vectors b_{DF} and b_{TO}, as well as vectors aDF and aTO, using the transfer functions $H_{DF}(z)$ and $H_{TO}(z)$, eqs. (16), (17) and (18). If we change $b_1 = b_{0d}$, $b_2 = b_{1d}$, $b_3 = b_{2d}$, $a_1 = -a_{1d}$ and $a_2 = -a_{2d}$ in eq. (13), we can determine vectors b_{τ} and a_{τ} , which correspond to eq. (13). Vectors b_{τ} and a_{τ} are shown in eqs. (19) and (20). All of vectors are necessary for the frequency analyses of the described IIR FLL₃ and the digital filter, using Mat-lab tools intended for the IIR digital filters.

$$H_{TO}(z) = \frac{TO(z)}{TI(z)} = \frac{z^2 b_{0d} + z b_{1d} + b_{2d}}{z^2 + z a_{1d} + a_{2d}} \cdot z^{-1}$$
(14)

$$H_{DF}(z) = \frac{y(z)}{x(z)} = \frac{z^2 b_{0d} + z b_{1d} + b_{2d}}{z^2 + z a_{1d} + a_{2d}}$$
(15)

$$b_{DF} = [b_{0d} \ b_{1d} \ b_{2d}] \tag{16}$$

$$b_{TO} = \begin{bmatrix} 0 & b_{0d} & b_{1d} & b_{2d} \end{bmatrix} = \begin{bmatrix} 0 & b_{DF} \end{bmatrix}$$
(17)

$$a_{DF} = a_{TO} = [1 \ a_{1d} \ a_{2d}]$$
(18)

$$b_{\tau} = \begin{bmatrix} 0 & -1 & (b_{2d} + b_{1d} - 1) \\ (b_{2d} + b_{1d} - a_{2d} - a_{1d} - 1) \end{bmatrix}$$
(19)

$$a_{\tau} = a_{DF} = a_{TO} = [1 \ a_{1d} \ a_{2d}]$$
(20)

After we developed vectors "a" and "b", based on the transfer functions of the IIR FLL₃ outputs TO and τ , the further procedure of frequency analysis of these outputs can be performed in a completely identical way. In the following text, we will give the emphasis to the design and analysis of the filter characteristics of IIR FLL₃ using output TO and comparing it with the corresponding digital filter. In order to design an IRR FLL₃, we have to first design the corresponding IIR digital filter of the second order (IIR DF₂). Let us design Butterworth low pass IIR DF₂, defined by the cutoff frequency fg=2000 Hz and sampling frequency fs=10000 Hz. Using Mat-lab command $[b_{DF}, a_{DF}] =$ butter (N, f_n), where the filter order N=2 and $f_n=f_g/(f_s/2)$, we can get vectors b_{DF} =[0.1867 0.3734 0.1867] and $a_{DF} = [1 -0.4629 \ 0.2097]$. Note that eq. (10) is satisfied, if we change $b_1=0.1867$, $b_2=0.3734$, $b_3=0.1867$, $a_1=-(-0.4629)$ and $a_2=-$ 0.2097. This means that after changing the parameters with the coefficients of IIR DF₂, IIR FLL₃ will stay stable. Since vectors b_{DF} and aDF are now determined, bTO and aTO are defined by eqs. [17] and [18]. Based on these vectors and using Mat-lab commands freqz (b_{TO}, a_{TO}, 1024, fs) and freqz (b_{DF}, a_{DF}, 1024, fs), the frequency responses of IRR FLL3 and IRR DF₂, are determined and presented in Fig.

2 for the half of the sample rate. It can be seen that the magnitudes of the IIR DF₂ and IIR FLL₃ are identical. Since both of IRR FLL₃ and IRR DF₂ are the IIR digital filters, no one of their phases is linear, but for the half of the sample rate, the phase of IIR FLL₃ is -360° and the phase of IIR DF₂ is -180°. The phases which two systems introduced into the output signals differ for expected -180°, for the half of the sample rate. This proves that the adaptation of the third-order FLL, with the aim of functioning as a second-order IIR digital filter, has been successfully realized.



Fig. 2. Magnitudes and phases of the frequency responses of $H_{TO}(z)$ and $H_{DF}(z)$.

Let us demonstrate the filter characteristics of the Butterworth low pass digital filter based on the designed third-order FLL. Suppose that the input period TI_{k+1} is defined as TI(k+1)= $6+S_1(k)+S_2(k)$ [time units], where $S_1(k)=$ $5 \cdot \sin[2\pi/f_{s} \cdot f_{1} \cdot k]$ and $S_2(k)=5\cdot\sin[2\pi/f_s\cdot f_2\cdot k]$. The input periods are continuously changing under effects of two sinusoidal signals S1 and S_2 . Suppose that the values of frequencies are $f_1=1000$ Hz and $f_2=4000$ Hz. Note that the frequency f_1 is less than the cutoff frequency $f_g=2000$ Hz and the frequency f_2 is greater than fg. The time unit [t.u.] can be, usec, msec or any other, but assuming the same time units for all time variables. It was more suitable to omit [t.u.] in the diagrams. The first step in this presentation is to form vector TI of 10000 values of TI, using the above equation for TI_{k+1} . Based on the vector TI, the output period vector $TO = filter (b_{TO}, a_{TO}, TI)$ is

determined. This vector can also be formed on the basis of eqs. (3) and (4). After that, using the "fft" command, the input and output vectors of IIR FLL₃ are formed as X = fft (TI) and Y = fft (TO). Finally, using the command "stem", stem (abs (X)) and stem (abs (Y)), the spectrums of the input and output periods are presented in Fig. 3. These spectrums present the absolute values of the amplitudes, covering the whole sample rate. They appear as positive values in the symmetric second half of the sample rate. It is visible in Fig. 3 that signal S₁ at 1000 Hz, is only slightly attenuated, since f₁ is less than cutoff frequency f_g=2000 Hz. This



Fig. 3. The input spectrum of TI and the output spectrum of TO.

agrees with magnitude of the IIR FLL₃ frequency response shown in Fig. 2, since at $f_1=1000$ Hz, the attenuation is close to zero. At the same time signal S₂ at 4000 Hz is suppressed for about -24.5 dB in Fig. 2, because $f_2=4000$ Hz is greater than cutoff frequency f_g . It can be seen in Fig. 3, that the zero component at the frequency close to zero is not attenuated, what is also in agreement with the magnitude of IIR FLL₃, shown in Fig. 2. A more complete description regarding the zero component are presented in ref. [1] and [2].

CONCLUSION

Unlike refs. [1] and [2] which describe the new kind of FIR digital filters based on the processing of the input periods only, this article presents the design of a new kind of an IIR digital filter, based on the processing of the input and output periods. Both of them use the theory, respectively, of the classical FIR and IIR digital filters. They are both of them intended for the filtering of impulse signal periods. This article represents an important contribution to the theory and application of new kind of IIR FLL digital filters, based on FLL. The shown adaption for the third order FLL to function as an IIR digital filter, using the theory of the classical IIR digital filters, can be applied to a FLL of any order.

This article opened the wide possibilities for the usage of IIR FLL digital filters in electronics, telecommunications, control and measurements, which use the different forms of periodic and non-periodic pulse signals. There is an obvious need to filter them in some of the applications.

However, the shown mathematical process of adaption can be very long and complex procedure, especially for a FLL of very high order, which is expected to be used in filtering. Because of that, in the next step, it is necessary to develop all the necessary equations, used in this adaptation, for FLL of the N-th order. This will enable short, simple and safe adaptation, which will be almost reduced to the development of a classical IIR digital filter.

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REFERENCE

- [1]Dj. M. Perišić, V. Petrović, B. Kovačević, "Frequency Locked Loop based on the Time Nonrecursive Processing", Engineering, Technology & Applied Science Research, Vol. 8, No. 5, p. 3450-3455, 2018.
- [2] Dj. M. Perišić, M. Bojović, Ž. Gavrić, V. Mišković, "New kind of digital filter based on the Frequency Locked Loop", International Sc. Conf. UNITECH 2020, Gabrovo Bulgaria, proceedings, Vol. I, pp. 167-172, 2020.
- [3]Dj. M. Perišić, A. Žorić, Ž. Gavrić, N. Danilović, "Digital circuit for the averaging of the pulse periods", Rev. Roum. Sci. Techn. Électrotechn. et Énerg., vol. 63, 3, pp. 300–305, Bucarest, 2018.
- [4] Dj. M. Perišić, A. Žorić, Ž. Gavrić, "A frequency multiplier based on the Time Recursive Processing", Engineering,

Technology & Applied Science Research, Vol. 7, No. 6, 2104-2108, 2017.

- [5] Dj. M. Perišić, M. Bojović, "Application of Time Recursive Processing for the development of the time/phase shifter", Engineering, Technology & Applied Science Research, Vol. 7, No. 3, 1582-1587, 2017.
- [6] Dj. M. Perišić, M. Bojović, "Multipurpose Time Recursive PLL", Rev. Roum. Sci. Techn. – Électrotechn. et Énerg., Vol.61, 3, pp. 283– 288, 2016.
- [7] Dj. M. Perišić, A. Žorić, M. Perišić, D. Mitić, "Analysis and Application of FLL based on the Processing of the Input and Output Periods", Automatika 57 (2016) 1, ISSN 0005-1144, DOI: 10.7305/automatika. 2016.07.769, pp. 230–238, 2016.
- [8] Dj. M. Perišić, M. Perišić, D. Mitić, M. Vasić "Time Recursive Frequency Locked Loop for the tracking applications", Rev. Roum. Sci. Techn. – Électrotechn. et Énerg., 60, 2, pp. 195–203, 2015.
- [9] Dj. M. Perišić, M. Perišić, D. Mitić, M. Vasić "Time Recursive Frequency Locked Loop for the tracking applications", Rev. Roum. Sci. Techn. – Électrotechn. et Énerg., 60, 2, pp. 195–203, 2015.
- [10] Dj. M. Perišić, M. Perišić, S. Rankov, "Phase shifter based on a Recursive Phase Locked Loop of the second order", Rev. Roum. Sci. Techn. – Électrotechn. et Énerg., 59, 4, pp. 391–400, 2014.
- [11] Dj. M. Perišić, A. Žorić, Dj. Babić, Dj. Dj. Perišić, "Decoding and Prediction of Energy state in Consumption control", Rev. Roum. Sci. Techn. – Électrotechn. et Énerg., 58, 3, pp. 263–272, 2013.
- [12] G. Bianchi, "Phase-Locked Loop Synthesizer Simulation", *Nc-Hill, Inc.*, New York, USA (2005).
- [13] M, Gardner, "Phase lock techniques", *Hoboken*, Wiley-Interscience, (2005).
- [14] B. D. Talbot, "Frequency Acquisition Techniques for Phase Locked Loops", *Wiley-IEEE Press*, pp. 224 (2012).
- [15] R. Vich, "Z Transform Theory and Application (Mathematics and Applications)", *Ed. Springer*, (1987-first edition).
- [16] A. K. Maini, Digital Electronics, principles, devices and applications, John Wley and Sons, Ltd, 2007.
- [17] S. Winder, Analog and Digital Filter Design (second edition), Copyright©2002 Elsevier Inc., 2002).