

# USAGE OF FREQUENCY LOCKED LOOP FOR TIME-DIGITAL FILTERING AND OTHER APPLICATIONS

Djurdje M. Perišić<sup>1</sup>, Aleksandar Žorić.<sup>2</sup>

<sup>1</sup> Technology, Slobomir P Faculty of Information University, Slobomir, BiH <sup>2</sup> Faculty of Technical Science, K. Mitrovica, Serbia

#### Abstract

This article describes development, analysis, application, and simulation of a recursive Time Frequency Locked Loop (TFLL) based on the measurement and processing of the periods of the input and output signals. TFLL is a linear discrete system of the second order, which regulates its output once per the input period. The parameters of TFLL are determined by the ratios of clock frequencies which have to be in the predefined relationships for the stable functioning of TFLL. Mathematical description, analysis of stability conditions and properties of TFLL are performed using Z transform. The relations of the parameters which correspond to the specific applications are analyzed. Using mathematical analyses and simulations, it is shown that TFLL is suitable for powerful noise rejection, for the different predicting and tracking applications, for the measurements of the frequency, for the filtering of impulse signal periods as well as for the other usual applications of FLL. Special emphasis is given to the development of a Time digital filter based on TFLL, using the theory of digital filters.

Keywords: Digital circuits, Frequency locked loops, Phase locked loops, Digital filters, Linear discrete system.

# **INTRODUCTION**

Time Frequency Locked Loop (TFLL), Time Phase Locked Loop (TPLL) and Time digital filter are based on the processing of the periods of the input and output impulse signals and time differences between them. They are recently described in refs. [1 to 12]. They represent one fundamentally new approach in the scientific literature from the point of view of constructions, descriptions, way of signal processing, way of analyzes and applications. The applications of these systems are numerous. In addition to digital filtering of the pulse signal period, they are applied in the field of tracking and prediction, phase and time shifting, frequency multipliers, frequency synthesizers, noise rejections, averaging of the periods, frequency measurement and the others.

In this article, we will present the full recursive second-order TFLL model, perform various analyses in the time and frequency domain, make simulations and describe the development of Time digital filter, based on TFLL. In addition, we will demonstrate some of its applications.

The articles and books [13-24] are used as the theoretical and mathematical base.

## **DESCRIPTION AND ANALYSIS OF FLL**

The general case of the input and output signals Sin and Sop for TFLL of M-th order, is shown in Fig. 1. Periods  $TI_k$  and  $TO_k$ , as well as the time differences  $\tau_k$ , occur at discrete times  $t_k$ ,  $t_{k+1}$ ,  $t_{k+2}$ ,... $t_{k+M-1}$ ,  $t_{k+M}$ , which are defined by the falling edges of the pulses of Sop. The difference equations of full second-order TFLL<sub>2</sub> are presented in eq. (1), where



*Fig. 1. Time relations between the input and output variables of TFLL of the M-th order.* 

 $b_1$ ,  $b_2$ ,  $a_1$  and  $a_2$  are the system parameters of FLL<sub>2</sub>. One additional natural relation between the time variables, which yields from Fig. 1, is shown in eq. (2). In order to found the transfer functions of TFLL<sub>2</sub> let us find the Z transforms of eqs. (1) and (2). They are presented in eqs. (3) and (4). If we calculate TO(z) from eq. (3), we can after that substitute  $TO_1 = b_1 TI_0 + a_1 TO_0$  into TO(z)and get the final expression for TO(z), given by eq. (5). Note that the previous expression for  $TO_1$  comes out from eq. (1), for k = -1. If we substitute now TO(z) from eq. (5) into eq. (4), we can found out the expression for  $\tau(z)$ , shown in eq. (6). Two transfer functions describing TFLL<sub>2</sub>, which are given by (7) and (8), can be defined from (5) and (6). Note that TO<sub>0</sub>, TI<sub>0</sub> and  $\tau_0$ in eqs. (3) and (4) are the initial conditions of TO<sub>k</sub>, TI<sub>k</sub> and  $\tau_k$ .

$$TO_{k+2} = b_1 \cdot TI_{k+1} + b_2 \cdot TI_k + a_1 \cdot TO_{k+1} + a_2 \cdot TO_k$$
 (1)

$$\tau_{k+1} = \tau_k + TO_k - TI_k \tag{2}$$

$$z^{2}TO(z) - zTO_{1} - z^{2}TO_{0} = z \cdot b_{1} \cdot TI(z) - z \cdot b_{1} \cdot TI_{0} + b_{2} \cdot TI(z) + z \cdot a_{1} \cdot TO(z) - z \cdot a_{1} \cdot TO_{1} + a_{2} \cdot TO(z)$$
(3)

$$z \cdot \tau(z) - z \cdot \tau_0 = \tau(z) + TO(z) - TI(z)$$
(4)

$$TO(z) = TI(z) \frac{z \cdot b_1 + b_2}{z^2 - z \cdot a_1 - a_2} + z \frac{z \cdot TO_0}{z^2 - z \cdot a_1 - a_2}$$
(5)

$$\tau(z) = TI(z) \frac{-z - (b_2 + a_2)}{z^2 - z \cdot a_1 - a_2} +$$
(6)

$$\frac{z}{z-1}\frac{z\cdot IO_0}{z^2-z\cdot a_1-a_2} + \frac{z\cdot \tau_0}{z-1}$$

$$H_{TO}(z) = \frac{TO(z)}{TI(z)} = \frac{z \cdot b_1 + b_2}{z^2 - z \cdot a_1 - a_2}$$
(7)

$$H_{\tau}(z) = \frac{\tau(z)}{TI(z)} = \frac{-z - (b_2 + a_2)}{z^2 - z \cdot a_1 - a_2}$$
(8)

In order to analyze TFLL<sub>2</sub>, let us suppose that the step function TI(k)=TI=constant is applied to the input. Z transform of TI(k) is TI(z)=TI·z/(z-1). If we enter TI(z) into eq. (5), using the final value theorem, it is possible to find TO<sub> $\infty$ </sub>=limTO(k) if  $k\rightarrow\infty$ , using TO(z). This is shown in eq. (9). It comes out from eq. (9) that TFLL<sub>2</sub> will be functional if eq. (10) is satisfied. Only in this case TO<sub> $\infty$ </sub>=TI.

$$TO_{\infty} = \lim[(z-1) \cdot TO(z)]_{z \to 1} = TI \cdot \frac{b_1 + b_2}{1 - a_1 - a_2}$$
 (9)

$$a_1 + a_2 + b_1 + b_2 = 1 \tag{10}$$

In the same way, the final value of  $\tau(k)$  if  $k \rightarrow \infty$  can be determined. Providing that the step function TI(k)=TI is applied to the input, Tl(z) in eq. (6) should be substituted by  $TI \cdot z/(z-1)$ . We can find out the final value  $\tau_{\infty} = \lim [\tau(k)]_{k \to \infty}$ , using the final value theorem in Z transform notation  $\tau_{\infty} = \lim[(z-1) \cdot \tau(z)]_{z \to 1}.$ this Applying expression, we can get  $\tau_{\infty}$ , shown in eq. (11). As we can see from eq. (11), time difference  $\tau_{\infty}$  in the stable state of TFLL<sub>2</sub> depends on the initial conditions  $\tau_0$  and  $TO_0$ , as well as on the system parameters and constant input period TI. That means TFLL<sub>2</sub> does not possess the properties of a PLL.

$$\tau_{\infty} = -TI \frac{b_{2} + a_{2} + 1}{1 - a_{1} - a_{2}} + \frac{TO_{0}}{1 - a_{1} - a_{2}} + \tau_{0}$$
(11)

All the previous conclusions, including the results given by eqs. (9), (10) and (11), are valid only if the system is stable. TFLL<sub>2</sub> is the stable system if the poles  $|z_1| < 1$  and  $|z_2| < 1$ , where  $z_1$  and  $z_2$  are the zeros of the polynomial  $z^2$ -z·a<sub>1</sub>-a<sub>2</sub> in eq. (7) or in eq. (8). The zeros  $z_1$  and  $z_2$  are shown in eq. (12).

$$z_{\frac{1}{2}} = \frac{a_1}{2} \pm \sqrt{\left(\frac{a_1}{2}\right)^2 + a_2}$$
(12)

The conditions  $|z_1| < 1$  and  $|z_2| < 1$  define the region in the plane of parameters  $b_1$  and  $b_2$ , where TFLL is the stable system. This region, shown in Fig. 2, is located between three mathematical straight lines defined by  $a_2=-1$ ,  $a_2=a_1+1$  and  $a_2=-a_1+1$ .



Fig. 2. Figure shows the region of  $a_1$  and  $a_2$  for the stable  $TFLL_2$ .

In order to investigate the tracking performances of TFLL<sub>2</sub> we will analyze the behavior of  $TO_k$  and  $\tau_k$  for the ramp input. All time variable will be expressed in time units (t.u.). Note that t.u. can be  $\mu$ s, ms, or any other time unit, assuming the same time unit for all time variables. Because of simplicity, "t.u." units are omitted from the diagrams.

Let us denote the tracking error of TO<sub>k</sub> by  $K_{TOV}$  and the final value of time difference  $\tau_k$  by  $\tau_{V\infty}$ , where "v" denotes that the input period is a velocity function  $TI_V(k)=c\cdot k$  and "c" is the constant. Using the final value theorem,  $K_{TOV}$  and  $\tau_{V\infty}$  can be expressed by Z transform notation, as in (14). and we eqs. (13)If enter  $Z[TI_V(k)] = TI_V(z) = z \cdot c/(z-1)^2$  into eq. (13), we will get  $K_{VTO}$ , given by eq. (15). It is obvious that  $K_{VTO}$  can be equal to zero only if eq. (16) is satisfied. In order to calculate  $\tau_{V\infty}$  we will first substitute  $b_2+a_2=-1$  into (6), that is,  $\tau_V(z)$  will get the simplified form. If we after that enter  $\tau_V(z)$  in (14), we can calculate  $\tau_{V\infty}$ , shown in eq. (17).

$$K_{TOV} = \lim \{ (z-1) \cdot [TO(z) - TI_V(z)] \}_{z \to 1}$$
(13)

$$\tau_{V\infty} = \lim[(z-1) \cdot \tau_V(z)]_{z \to 1}$$
(14)

$$K_{VTO} = c \cdot \frac{b_2 + a_2 + 1}{a_1 + a_2 - 1} \tag{15}$$

$$b_2 + a_2 = -1 \tag{16}$$

$$\tau_{V\infty} = \frac{-c}{1 - a_1 - a_2} + \frac{TO_0}{1 - a_1 - a_2} + \tau_0 \tag{17}$$

Let us now analyze the abilities of TFLL<sub>2</sub> for the tracking of the velocity input (ramp) using the simulations. The simulations of  $TO_k$ , K<sub>VTOk</sub> and  $\tau_{Vk}$ , for TI<sub>k</sub>=20+5·k [t.u.] is shown in Fig. 3. The simulation is made for combinations three of the system parameters and the initial conditions, which are presented in Fig. 3. Note that the first and the second combinations of parameters  $b_1$ ,  $b_2$ ,  $a_1$ , and  $a_2$ , signed by "1" and "2" satisfy both, eq. (16) and eq. (10). Due to this fact, the output periods of  $TO_{1k}$  and  $TO_{2k}$  track  $TI_{Vk}$  without error. The corresponding errors K<sub>VTO1k</sub> and K<sub>VTO2k</sub> tend to zero. Since K<sub>VT01</sub>=K<sub>VT02</sub>=0, the

simulation results agree with eq. (15). Note also that, for the first two combinations of parameters, the corresponding  $\tau_{1k}$  and  $\tau_{2k}$ respectively  $\tau_{VI\infty}$  and tend to  $\tau_{V2\infty}$ . According to eq. (17),  $\tau_{VI\infty}$ =-10.43 and  $\tau_{V2\infty}$ =38.50. After only eight steps  $\tau_{1k}$ =-10.51 and  $\tau_{2k}$ =38.48, proving so the of (17). correctness eq. The third combination of parameters, signed by "3" in Fig. 3, satisfies eq. (10), but it does not satisfy eq. (16). That means TFLL is the stable system but it is not adapted to track the ramp without error. We can see in Fig. 3 that the corresponding  $TO_{k3}$  tracks the input  $TI_{Vk}$  but with the constant error  $K_{VTO3}$ . According to eq. (15)  $K_{VTO3}$ =-6.25. After only eight steps, K<sub>VTO3</sub> is about to reach the final value K<sub>VTO3</sub>=-6.25, proving once more the correctness of all previous analysis.



*Fig. 3. Tracking of the input ramp function for three combinations of parameters.* 

The noise rejection ability of TFLL<sub>2</sub>, in the function of system parameters, is demonstrated by simulation and shown in Fig. 4. The input period  $TI_k$  is the step of 10 t.u., which is strongly corrupted by the uniform distributed noise. The amplitude of noise is 9 t.u. peak to peak. Three outputs  $TO_{1k}$ ,  $TO_{2k}$ , and  $TO_{3k}$  are presented in Fig. 4, depending on the different parameters for

the same input. In case of  $TO_{1k}$  where  $b_1$ and b<sub>2</sub> are very small, the output periods are completely cleaned from noise. For ten time higher  $b_1$  and  $b_2$ , the periods of  $TO_{2k}$  are a little noisy. At last, for the large  $b_1$  and  $b_2$ , the influence of the noisy input is the stronger in  $TO_{3k}$ . Note that, even for the worst case, noise in  $TO_{3k}$  is significantly reduced in comparison with the input noise. That means TFLL<sub>2</sub> is naturally suitable circuit for noise rejection applications. It can be concluded that if the sum of  $a_1$  and a<sub>2</sub> is grater, the influence of noisy input is smaller. Another important conclusion is that a larger sum of  $b_1$  and  $b_2$ , in Fig. 4, leads to a longer transition time of the TFLL<sub>2</sub>. In other words, for the better noise rejection, the TFLL<sub>2</sub> becomes automatically slower, i.e. its transition time becomes longer.



**Fig. 4.** The input is strongly corrupted by noise. The smaller values of  $b_1$  and  $b_2$  provides better noise rejection and the longer transition time.

# DESIGN OF IIR TIME DIGITAL FILTER

References [1, 2] show how to design a FIR (Finite Impulse Response) Time digital filter intended for filtering the period of an impulse signal based on TFLL. For this purpose, the theory of classical digital filters is used, as well as software tools from Mat-lab intended for the analysis and design of digital filters. In this article, we will describe the process of developing an IIR (Infinite Impulse Response) Time second-order digital filter based on TFLL. Using the corresponding TFLL, let us design a simple digital low-pass Butterworth filter with next properties: cutoff frequency (3 dB), cutoff frequency  $f_c=2$ kHz, minimum attenuation of 30 dB at stop band frequency, cutoff frequency of stop band  $f_b=4.25$  kHz and the sampling frequency  $f_s=10$  kHz. The first step is to design classical digital filter with the required properties. The transfer function H<sub>df</sub> of the second-order digital filter, which satisfies the requirements, is presented in (18).Note that  $b_{0d} = 0.20657$ , eq.  $b_{1d}=0.41315$ , b<sub>2d</sub>=0.20657,  $a_{1d}$ =-0.36953 and  $a_{2d}=0.19582$ . The frequency response of this filter is presented in Fig. (5).

$$H_{df}(z) = \frac{0.20657 \cdot z^2 + 0.41315 \cdot z + 0.20657}{z^2 - 0.36953 \cdot z + 0.19582} \quad (18)$$

Let us now determine the corresponding TFLL which is able to cover all zeros and poles of the transfer function  $H_{df}(z)$ . All zeros and poles can cover TFLL<sub>3</sub> whose difference equation is shown in eq. (19). That is the third-order TFLL<sub>3</sub>, but it is not full version. The part



**Fig. 5.** *Frequency response of Butterworth digital low-pass filter satisfies all requirements.* 

"a<sub>3</sub>·TO<sub>k</sub>" is missing from eq. (19). The transfer function of TFLL<sub>3</sub> is presented in eq. (20). We can see that the transfer functions  $H_{TO3}(z)$  and  $H_{df}(z)$  possess the same number of zeros and poles. If we now define  $b_1=b_{0d}=0.20657$ ,  $b_2=b_{1d}=0.41315$ ,  $b_3=b_{2d}=0.20657$ ,  $a_1=-a_{1d}=0.36953$  and  $a_2=-a_{2d}=-0.19582$ , eq. (20) will turn into eq. (21). By comparing eqs. (18) and (21), we can see that the ratio of  $H_{df}(z)$  and  $H_{TO3}(z)$  is given in eq. (22). They differ only for

factor "z<sup>-1</sup>". It means that the transfer function  $H_{TO3}(z)$  enters an additional delay of the input signal in comparison to the transfer function  $H_{df}(z)$ . This delay is  $2\pi$ [rad] for the full range of the sampling rate, i.e.  $\pi$  [rad] for the half of the sampling rate. The frequency response of the TFLL<sub>3</sub> is presented in Fig. (6). We can see in

$$TO_{k+3} = b_1 TI_{k+2} + b_2 TI_{k+1} + b_3 TI_k + a_1 TO_{k+2} + a_2 TO_{k+1}$$
(19)

$$H_{TO3}(z) = \frac{TO_3(z)}{TI(z)} = \frac{z^2 b_1 + z b_2 + b_3}{z^2 - z a_1 - a_2} \cdot z^{-1} \quad (20)$$

$$H_{TO3}(z) = \frac{0.20657z^2 + 0.41315z + 0.20657}{z^2 - 0.36953z + 0.19582} \cdot z^{-1} \quad (21)$$



**Fig. 6.** *Frequency response of Butterworth Time digital low-pass filter based on TFLL3.* 

Figs. (5) and (6) that the magnitudes of digital filter and TFLL<sub>3</sub> are identical, but the phases differ for expected  $-\pi$  [rad] for the half range of the sampling rate. The requirements for Time digital filter based on TFLL<sub>3</sub> are fulfilled, proving the correctness of the previous analysis and presentations.

$$H_{TO3}(z) = H_{df}(z) \cdot z^{-1}$$
 (22)

## CONCLUSION

The described design of TFLL represent the further deepening to the recently described theory, design and application of TFLL, TPLL and Time digital filter, presented in refs. [1] to [12].

This  $TFLL_2$  offers considerably wider possibility for the choice of the system parameters in comparison with similar TFLL of the first order, described in ref. [9]. In this respect TFLL<sub>2</sub> possesses better performances in the noise rejection applications. The advantages of this TFLL<sub>2</sub> are especially evident in the applications which require the trading of the extent of noise suppression and reduction of the system transient time. This TFLL<sub>2</sub> is also more powerful in the tracking applications in comparison to TFLL described in ref. [9]. Unlike TFLL in ref. [9] which is able to track the ramp input but with the constant error, this TFLL<sub>2</sub> provides the tracking of the ramp input without any error.

It is of interest to emphasize that TFLLs and the classical digital filters represent completely different types of systems. The first one is based on the processing of the periods of the impulse signals and time differences between them. In other word TFLLs process the time. The other ones are based on the processing of amplitudes. Regardless of that, the article showed that Mat-lab tools and the theory of IIR digital filters can be completely used for the analyzes and design of TFLLs in the frequency domain, as well as for the design of Time digital filters, based on TFLLs. In this work, it was shown practically how to understand the physical aspects of the TFLL processing, compared with the digital filter processing and how to identify the meanings of TFLL variables which we come across the usage of Mat-lab tools. Due to the mentioned contributions. Time Recursive Processing has got new weightiness and significance in the different scientific fields.

### Acknowledgements

This article was supported by the Ministry of Science and Technology of the Republic of Serbia within the project TR 47016.

### REFERENCE

 Dj. M. Perišić, "Digital filters intended for pulse signal periods", Rev. Roum. Sci. Techn. – Électrotechn. et Énerg., Vol. 67, 2, pp. 161–166, 2022.

- [2] Dj. M. Perišić, "Frequency locked loops of the third and higher order", Rev. Roum. Sci. Techn. – Électrotechn. et Énerg., Vol. 66, No. 4 pp. 261–266, (2021).
- [3]Dj. M. Perišić, V. Petrović, B. Kovačević, "Frequency Locked Loop based on the Time Nonrecursive Processing", Engineering, Technology & Applied Science Research, Vol. 8, No. 5, p. 3450-3455, 2018.
- [4] Dj. M. Perišić, M. Bojović, Ž. Gavrić, V. Mišković, "New kind of digital filter based on the Frequency Locked Loop", International Sc. Conf. UNITECH 2020, Gabrovo Bulgaria, proceedings, Vol. I, pp. 167-172, 2020.
- [5]Dj. M. Perišić, A. Žorić, Ž. Gavrić, N. Danilović, "Digital circuit for the averaging of the pulse periods", Rev. Roum. Sci. Techn. Électrotechn. et Énerg., vol. 63, 3, pp. 300–305, Bucarest, 2018.
- [6] Dj. M. Perišić, A. Žorić, Ž. Gavrić, "A frequency multiplier based on the Time Recursive Processing", Engineering, Technology & Applied Science Research, Vol. 7, No. 6, 2104-2108, 2017.
- [7] Dj. M. Perišić, M. Bojović, "Application of Time Recursive Processing for the development of the time/phase shifter", Engineering, Technology & Applied Science Research, Vol. 7, No. 3, 1582-1587, 2017.
- [8] Dj. M. Perišić, M. Bojović, "Multipurpose Time Recursive PLL ", Rev. Roum. Sci. Techn. – Électrotechn. et Énerg., Vol.61, 3, pp. 283–288, 2016.
- [9] Dj. M. Perišić, A. Žorić, M. Perišić, D. Mitić, "Analysis and Application of FLL based on the Processing of the Input and Output Periods", Automatika 57 (2016) 1, ISSN 0005-1144, DOI: 10.7305/automatika. 2016.07.769, pp. 230–238, 2016.
- [10] Dj. M. Perišić, M. Perišić, D. Mitić, M. Vasić "Time Recursive Frequency Locked Loop for the tracking applications", Rev. Roum. Sci. Techn. – Électrotechn. et Énerg., 60, 2, pp. 195–203, 2015.
- [11] Dj. M. Perisic, A. Zoric, M. Perisic, V. Arsenovic, Lj. Lazic, "Recursive PLL based

on the Measurement and Processing of Time", Electronics and Electrical Engineering, Vol. 20, No. 5, pp. 33-36, (2014).

- [12] Dj. M. Perišić, M. Perišić, S. Rankov, "Phase shifter based on a Recursive Phase Locked Loop of the second order", Rev. Roum. Sci. Techn. – Électrotechn. et Énerg., 59, 4, pp. 391–400, 2014.
- [13]). D. Jovcic, "Phase locked loop system for FACTS", IEEE Transaction on Power System, 18, pp. 2185-2192 (2003).
- [14]). C. C. Chung, "An all-digital phaselocked loop for high speed clock generation", IEEE Journal of Solid-State Circuits, **38**, Issue 2, pp. 347-359 (2003).
- [15] G. Bianchi, "Phase-Locked Loop Synthesizer Simulation", *Nc-Hill, Inc.*, New York, USA (2005).
- [16] M, Gardner, "Phase lock techniques", *Hoboken*, Wiley-Interscience, (2005).
- [17] B. D. Talbot, "Frequency Acquisition Techniques for Phase Locked Loops", Wiley-IEEE Press, pp. 224 (2012).
- [18] R. Vich, "Z Transform Theory and Application (Mathematics and Applications)", *Ed. Springer*, (1987-first edition).
- [19] A. K. Maini, "Digital Electronics, principles, devices and applications", John Wley and Sons, Ltd, 2007.
- [20] D. Abramovitch, "Phase-locked loops: a control centric tutorial", American Control Conference-2002, Proceedings of 2002, Vol 1, pp. 1-15, (2002).
- [21] S. Winder, "Analog and Digital Filter Design" (second edition), Copyright©2002 Elsevier Inc., 2002).
- [22]). S. W. Smith, "Digital Signal Processing" (second edition), California Technical Publishing, (1999).
- [23]). W. F. Egan, "Phase-Lock Basics" (second edition), John Wiley and Sons, (2008).
- [24]). C. B. Fledderman, "Introduction to Electrical and Computer Engineering", Prentis Hall, (2002).